

TVB140 DTV(VSB) Capture PCI Board



System Contents : TVB140 PCI Board + Bracket Assembly + CD + Manual

Overview

The DTV capture board TVB-140 is one of Teleview MPEG Stream boards that cover VSB reception and modulation boards as well as Transport Stream out. These product line shares common hardware architecture and API for easy adaptation of application software.

Features

- . ATSC 8VSB Reception
- . UHF(470MHz ~ 820MHz) from Terrestrial and Cable TV
- . On Board TTL Level RS232C Port
- . Real-time Loop-through Monitoring of Input Signal to LVDS Output
- . Seamless Local MPEG Stream Injection Preserving TS Continuity
- . Windows 2000/XP WDM, Linux device driver and API supported
- . Sample Play/Capture Software with Source Code in Visual Basic and Microsoft C
- . Downloadable EPLD Program Option Set
 - Play/Capture only
 - Play/Capture with Real Time TS Analysis
 - Play with Re-multiplexing (Multiplex Incoming TS with Playing TS)
- . Companion Boards
 - May use with Teleview 8VSB/QPSK/QAM Modulator PCI Card (TVB360)
 - May use with Teleview Audio/Video Decoder PCI Card (TMS491)

Specifications

VSB Input Connector: 75ohm F-type Format: ATSC A/53 Modulation: 8VSB LVDS TS Output* Connector: 26pin Header Level: LVDS Bit Rate: 1~60Mbps with 1Hz Step	On Board Buffer 16Mbytes Serial Interface RS232C Compatible TTL Level 115,200bps 8Bit no Parity Operating System Microsoft Windows 2000, XP, Linux	PCI Bus PCI Version 2.1 Compliant Physical/Environmental HxWxL:0.6 x 8.2 x 4.1(inch) Power Supply DC 5V 1.1A Typical
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*** DVB-ASI TS Output Option Available**

(Connector: 75ohm SMB(male),
Clock Rate : 270MHz +/- 100ppm,
Bit Rate: 1~60Mbps with 1Hz Step)